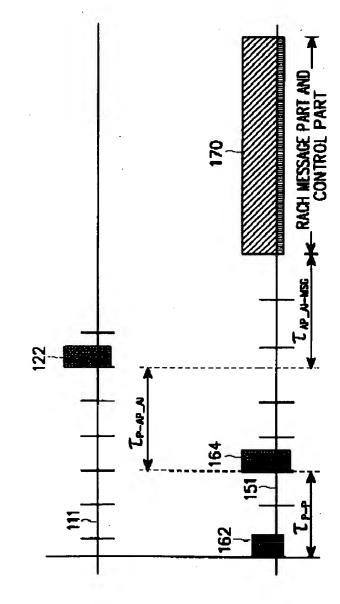
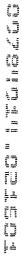
FIG. 1



POWER CONTROL AND PILOT FIG. 2 **20**6 215



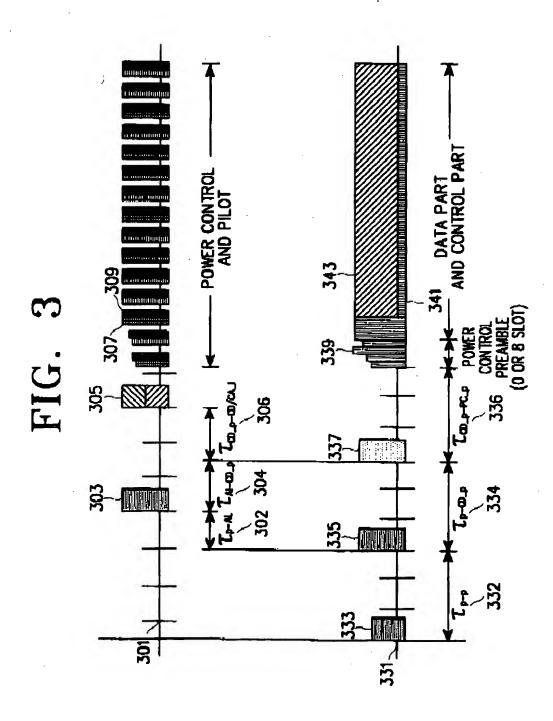
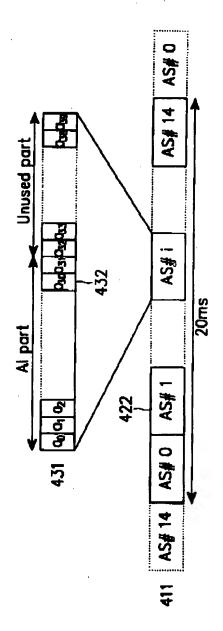


FIG. 4



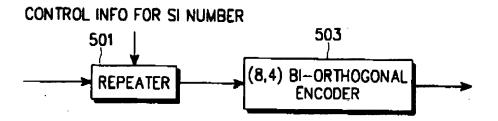


FIG. 5

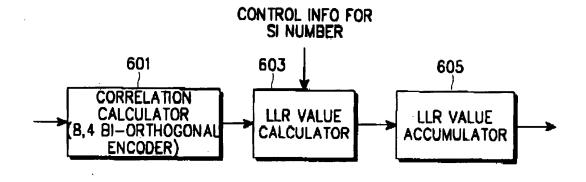
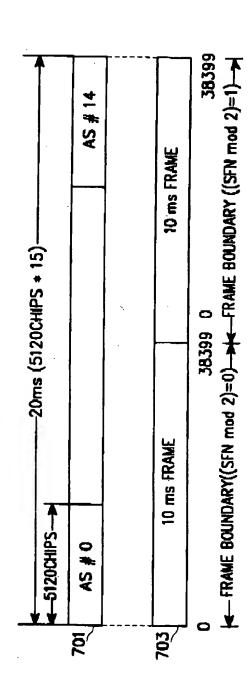


FIG. 6

to great gives reper event great. He is a super-cert is given a great of a great given than the standard with the standard given the standard gives the standard give

FIG. 7



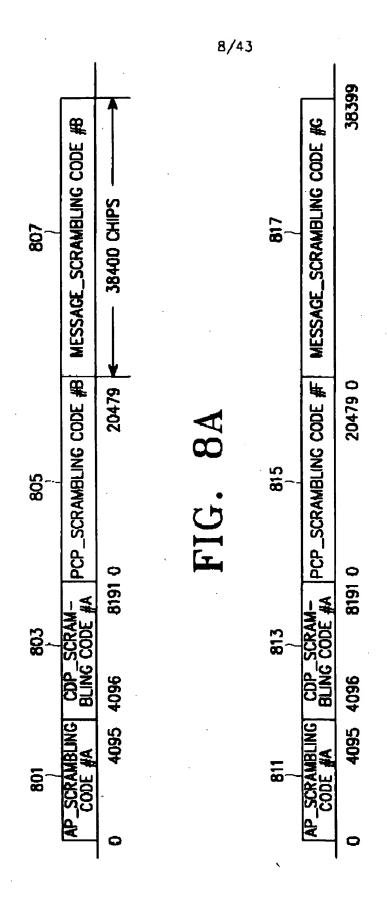


FIG. 8B

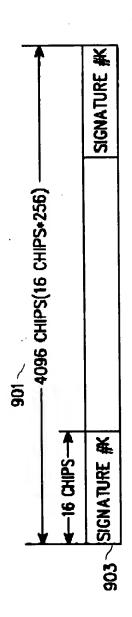


FIG. 9A

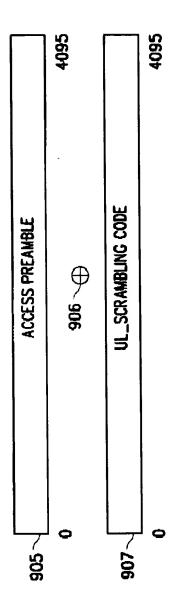


FIG. 9B

FIG. 10A

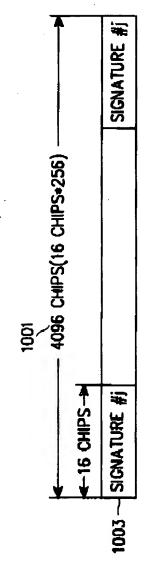


FIG. 10B

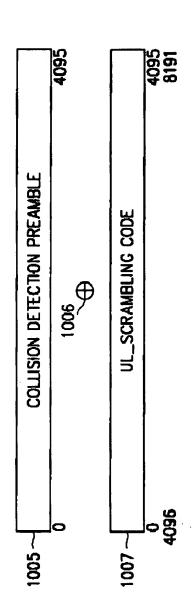
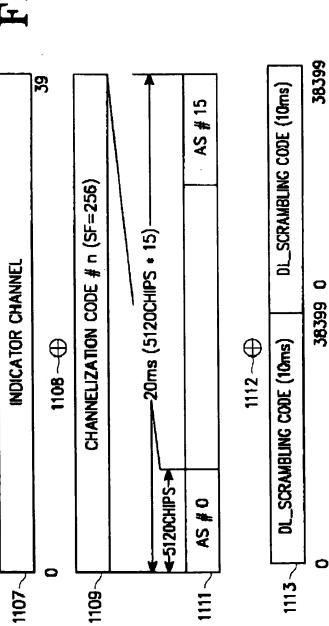


FIG. 11B



the first start start at the first start at the first start start with a

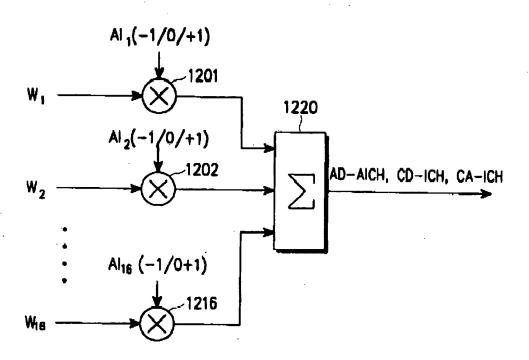


FIG. 12



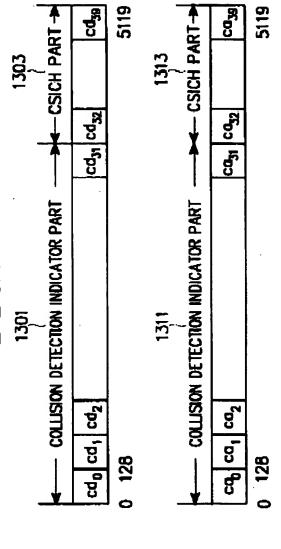
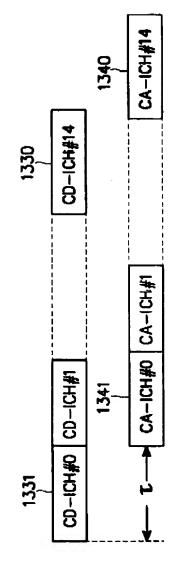


FIG. 13B



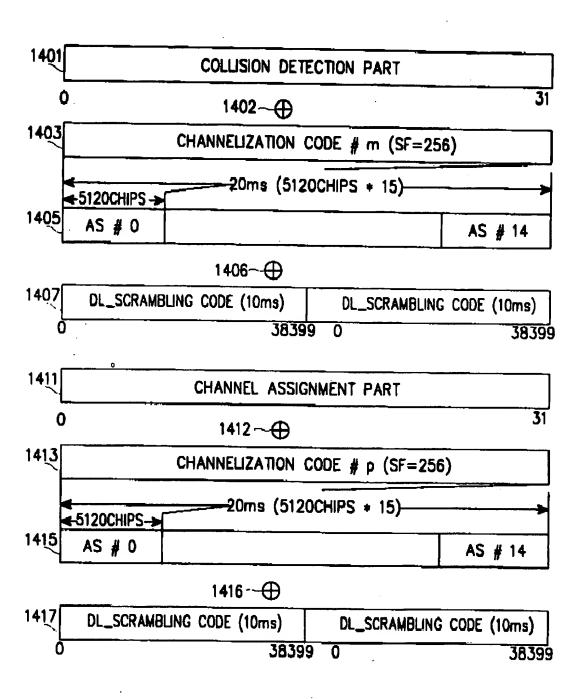


FIG. 14

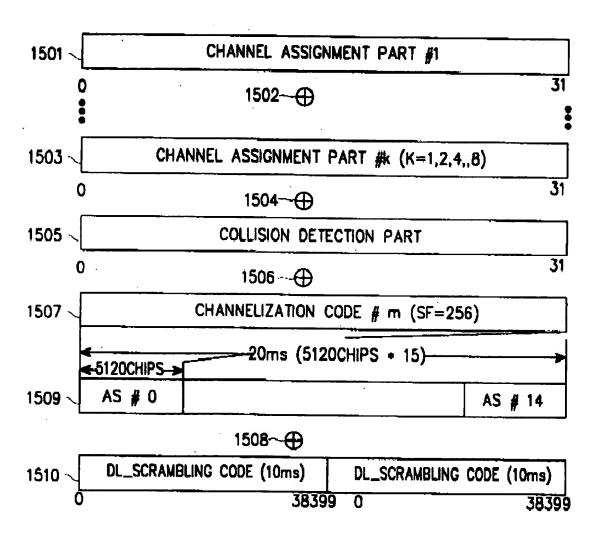


FIG. 15



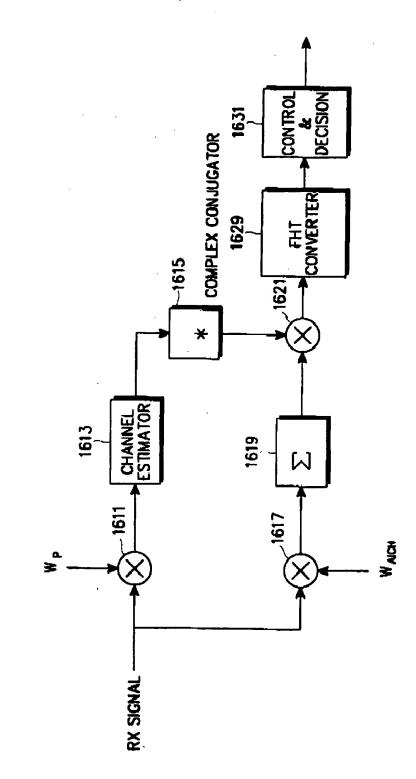
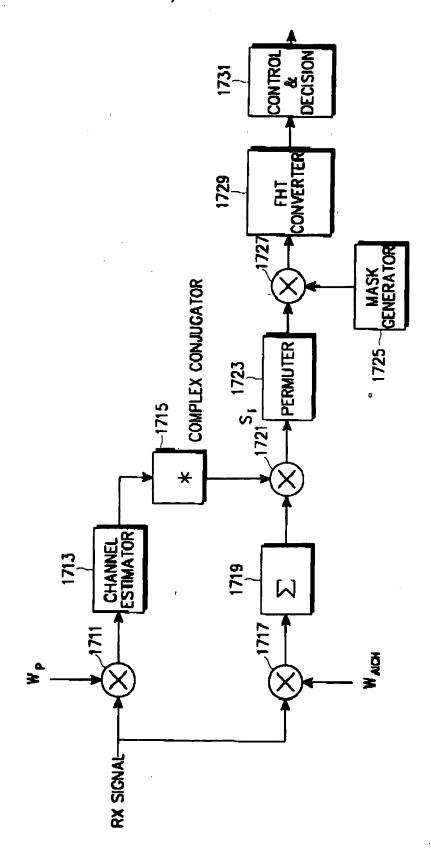


FIG. 17



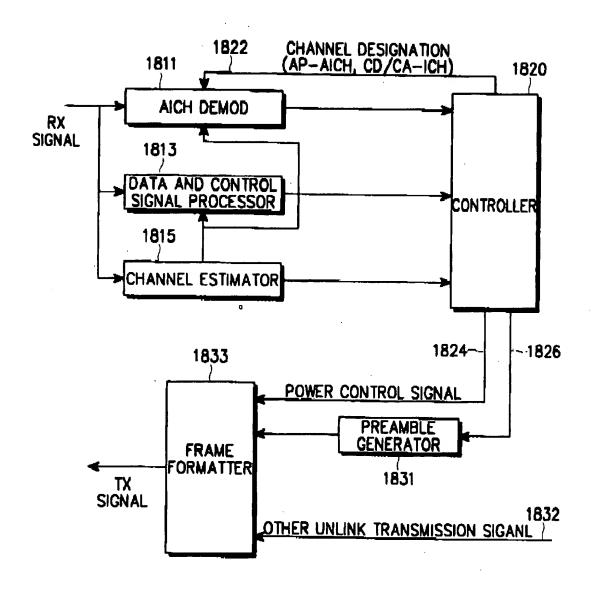
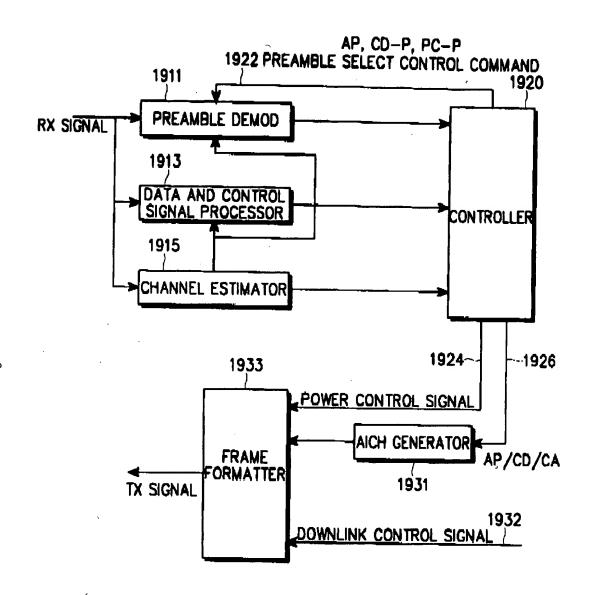


FIG. 18



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FIG. 19

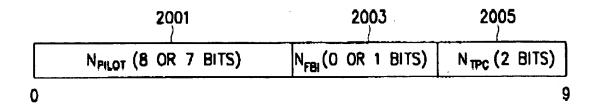


FIG. 20

FIG. 21

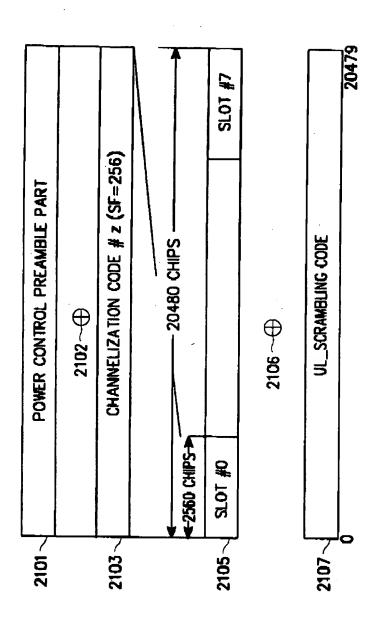
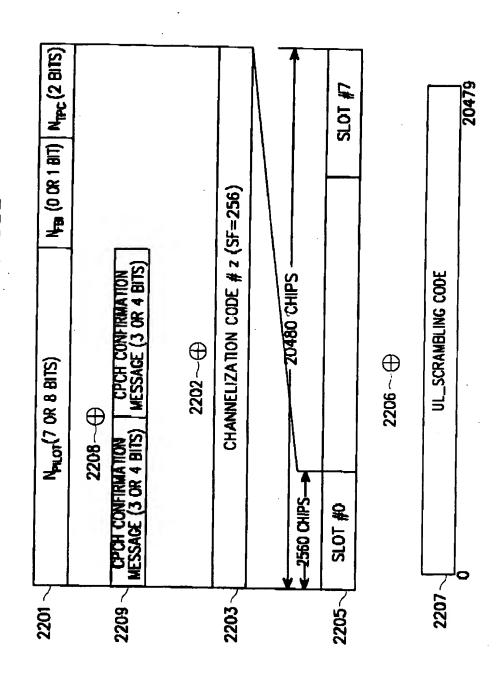


FIG. 22A



The Hard Color of the Color of

FIG. 22B

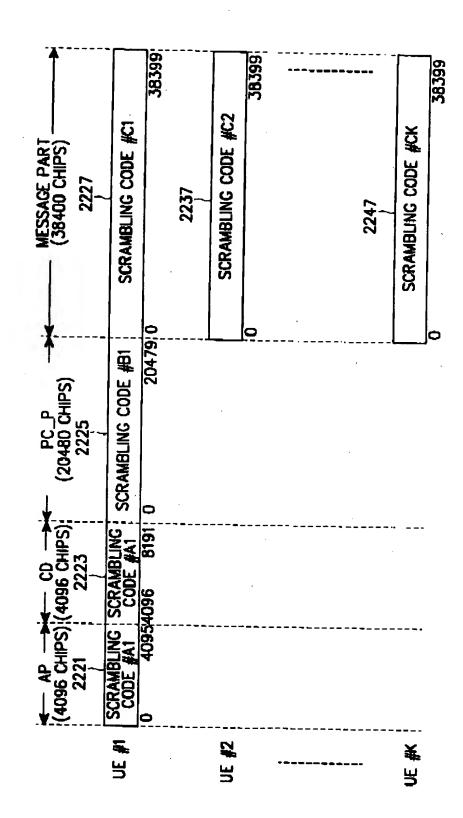


FIG. 23

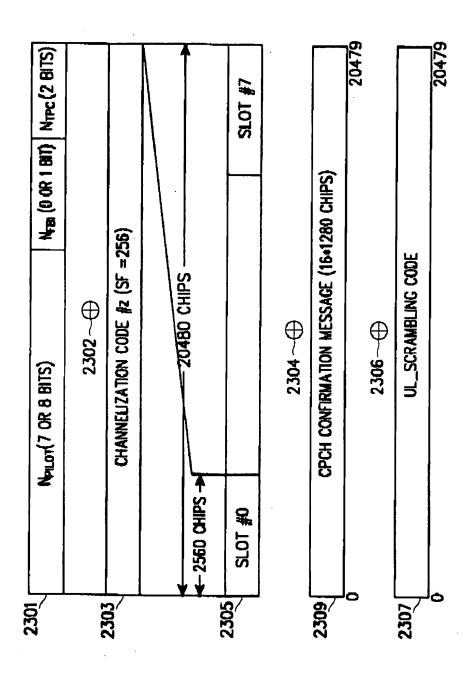
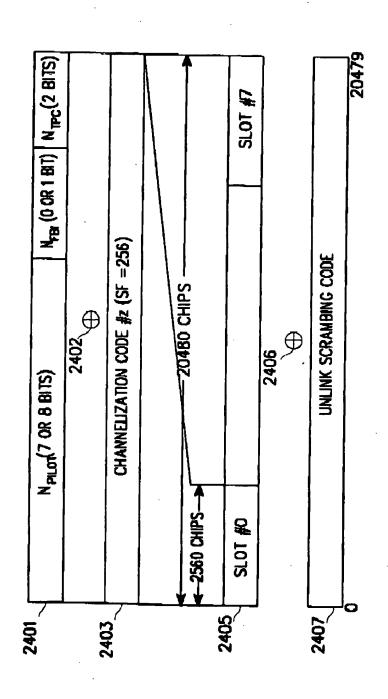


FIG. 24A



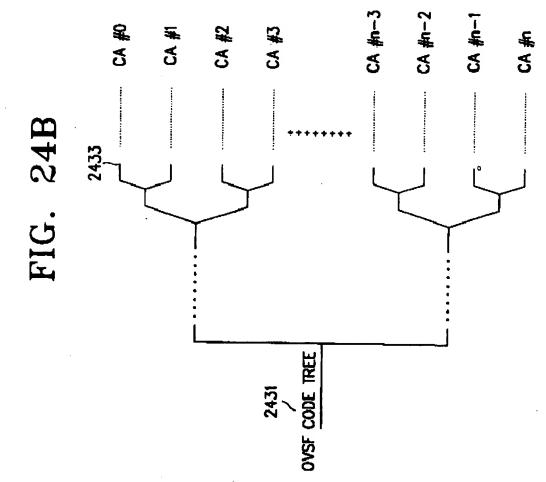


FIG. 25A

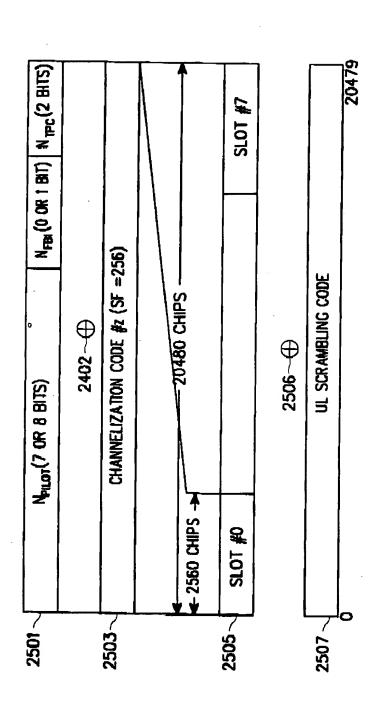
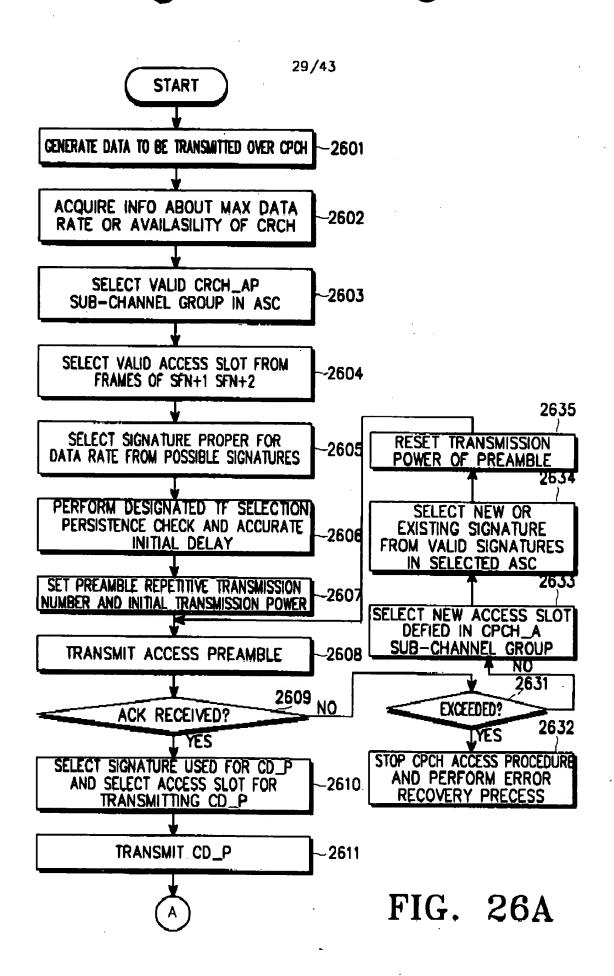


FIG. 25B

		38399			38399				38399
MESSAGE PART (38400 CHIPS) 2527	SCRAMBLING CODE #C1		2537	SCRAMBLING CODE #82			2547	SCRAMBLING CODE #BK	
(20480 CHIPS)	SCRAMBLING CODE #81	0 5047910	2535	SCRAMBLING CODE #82	0 20479 0		2545	SCRAMBLING CODE #BK	0 20479 0
(4096 CHIPS) 2523	SCRAMBLING CODE #A1	4096 8191							_
(4096 CHIPS) (4	SCRAMBLING CODE #A1	40954							
	UE #1			1F 47	7	क च्या राज के की		1	1

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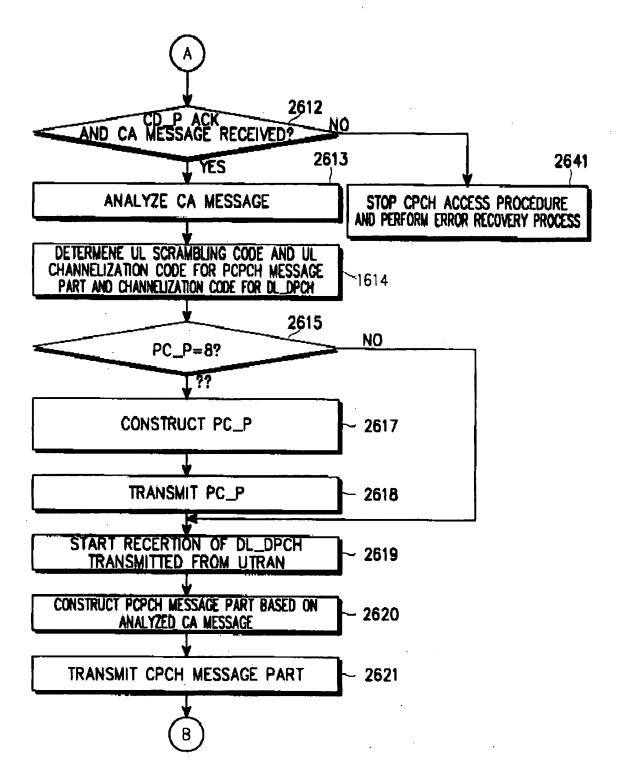


FIG. 26B

į.

FLOWCHART OF CPCH USING CHANNEL ASSIGNMENT (UE SIDE)

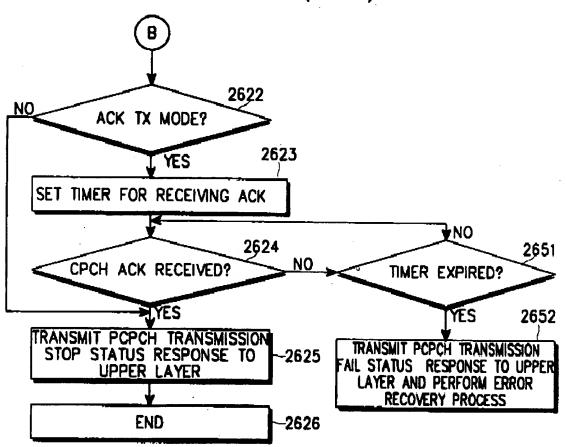


FIG. 26C

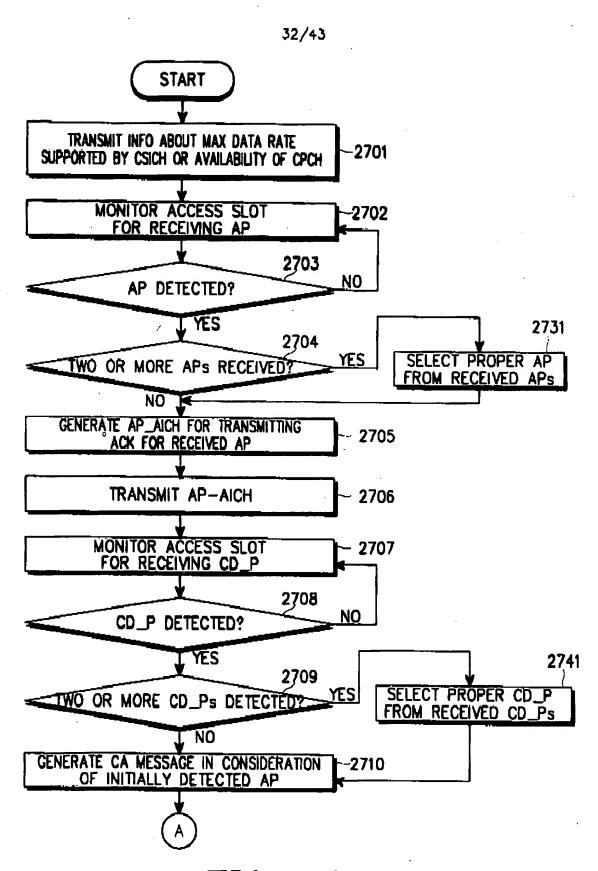


FIG. 27A

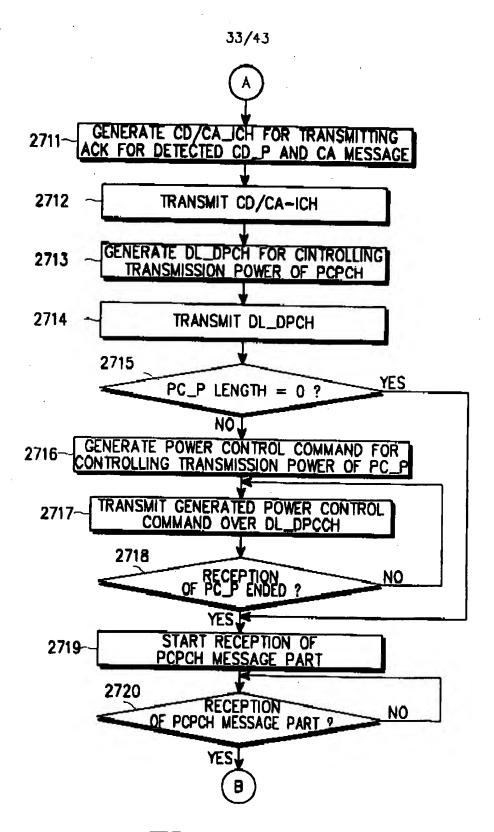


FIG. 27B

ļ.

FLOWCHART OF CPCH USING CHANNEL ASSIGNMENT (UTRAN SIDE)

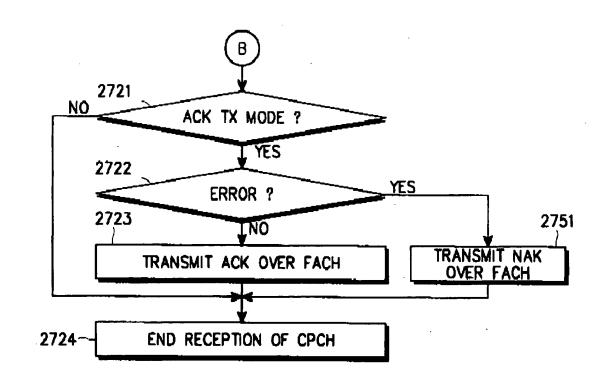


FIG. 27C

`

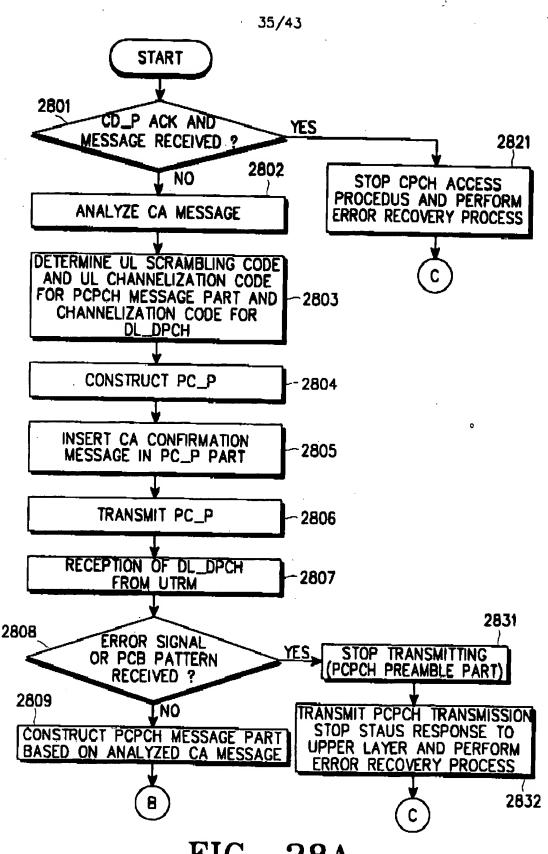


FIG. 28A

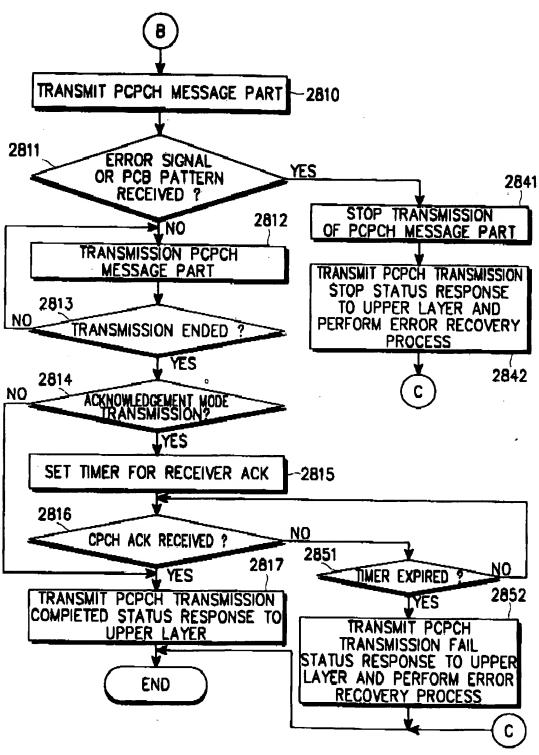
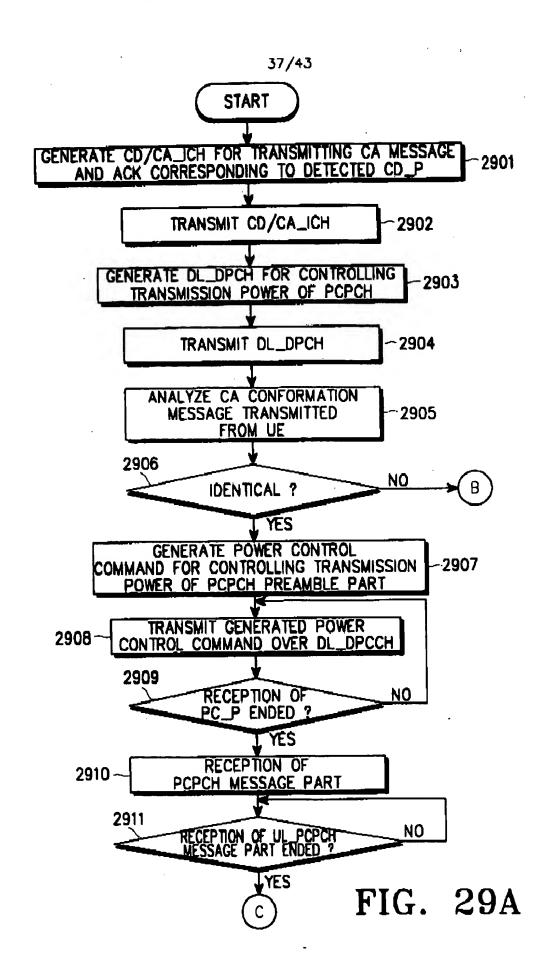


FIG. 28B



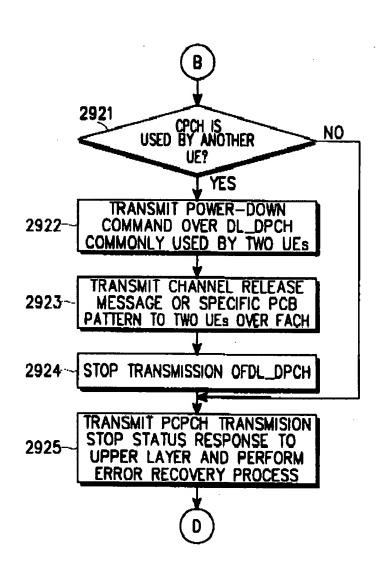


FIG. 29B

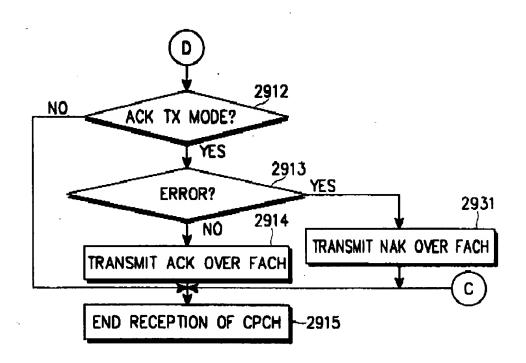


FIG. 29C

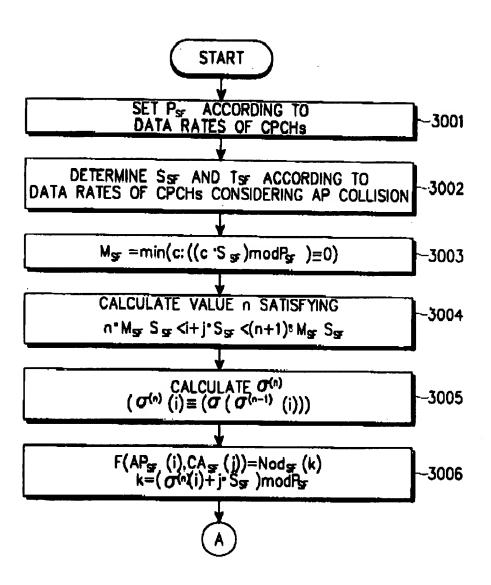


FIG. 30A

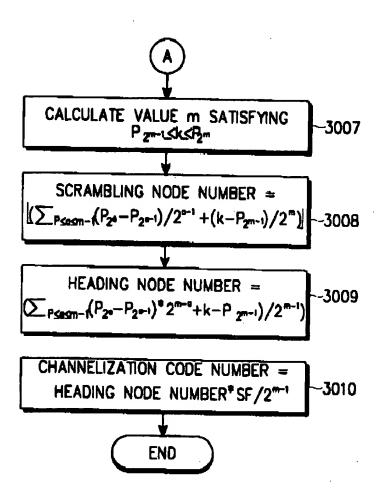


FIG. 30B

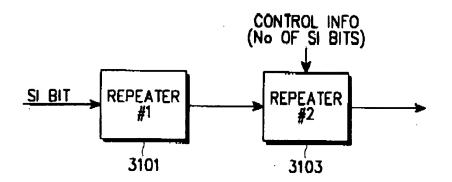


FIG. 31

